Vigo 1.6k bit R/W ISO15693 13.56 MHz RFID IC

Main Features

- ISO 15693 / ISO18000-3 standard compliant
- Operating frequency: 13.56MHz
- Long range, low power vicinity
- 64 bits ISO15693 UID
- 1664 bit EEPROM user memory (52 blocks of 32 bits)
- Fast Read Multiple Block mode (53 kbit/s or 13 kbit/s)
- EEPROM locking mechanisms
- 32 bit password to protect functionality and data
- EAS and AFI features
- 60 years memory data retention
- -40°C + 85°C operating temperature

Memory Organization

The chip memory is composed of 2 areas:

- User Memory from Block 0 up to Block 51
- Configuration area

A memory block is composed of 4 bytes (32 bits). A memory page is composed of 4 blocks.

User Block 0 0 User Block 1 1 User Block 2 2 User Block 3 3 User Block 44 44 User Block 45 45 User Block 46 46 User Block 47 47 User Block 48 48 User Block 49 49 User Block 50 50	Bit31	Bit0	Block	Page	
User Block 2 2 User Block 3 3 User Block 44 44 User Block 45 45 User Block 46 46 User Block 47 47 User Block 48 48 User Block 49 49	User Block 0 0				
User Block 2 2 User Block 3 3 User Block 44 44 User Block 45 45 User Block 46 46 User Block 47 47 User Block 48 48 User Block 49 49	User Bloc	k 1	1		
User Block 44	User Bloc	k 2	2	U	
User Block 44 44 User Block 45 45 User Block 46 46 User Block 47 47 User Block 48 48 User Block 49 49 12	User Bloc	k 3			
User Block 45 45 11 Data Memory User Block 47 47 User Block 48 48 User Block 49 49 12	User Block	: 44			
User Block 46 46 User Block 47 47 User Block 48 48 User Block 49 49	User Block	45	45	44	
User Block 49 49 12	User Block	46	46	''	
User Block 49 49	User Block	47	47		
12	User Block	: 48	48		
	User Block	: 49	49	12	
	User Block	: 50	50	12	
User Block 51 51	User Block	51	51		
		Memory			
Memory		configuration			
		Area			

Application

- Brand protection
- Library management
- Archives and collections
- Long-term asset management
- Pharmaceutical